

FIG. 1

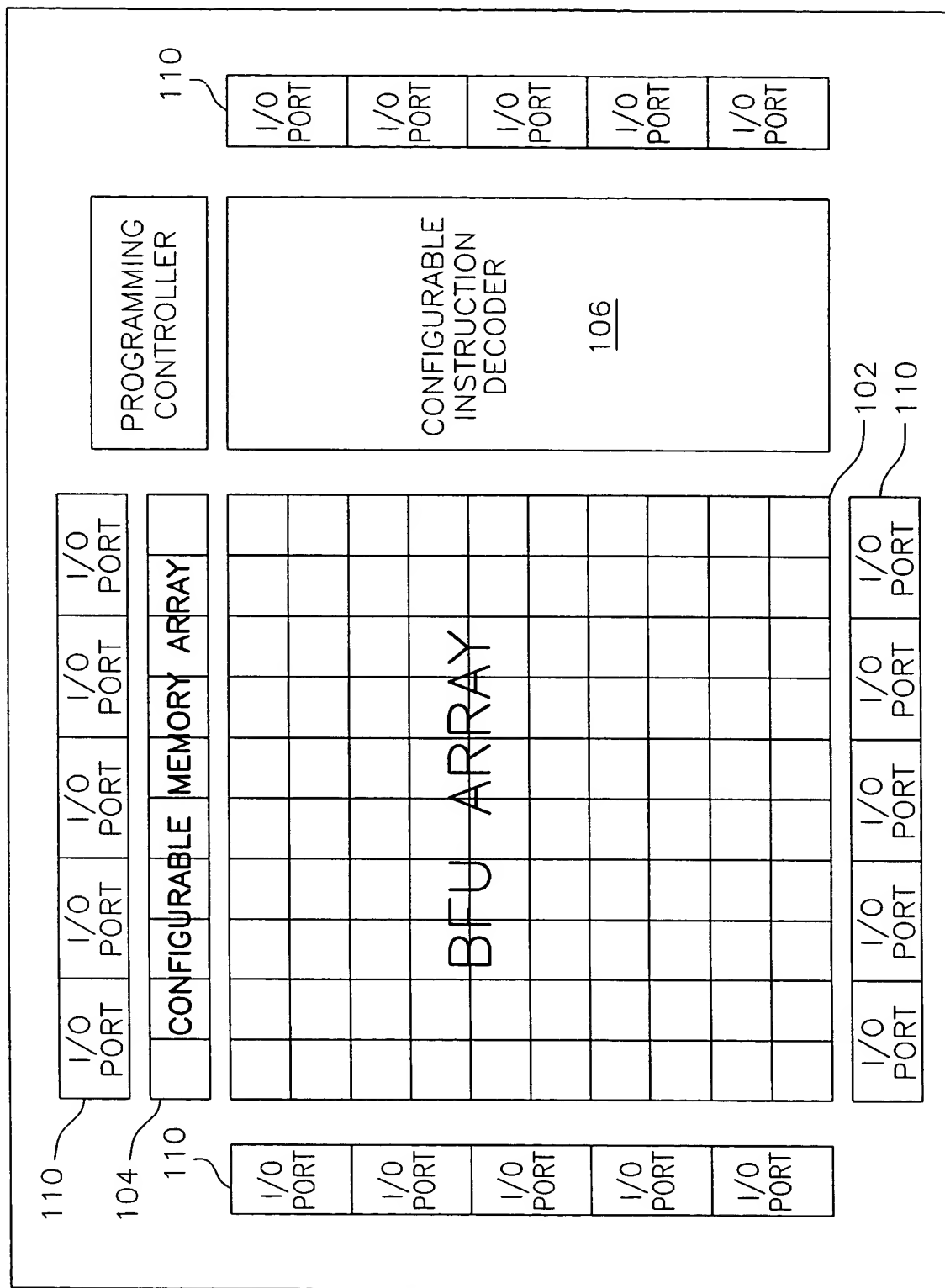


FIG. 2

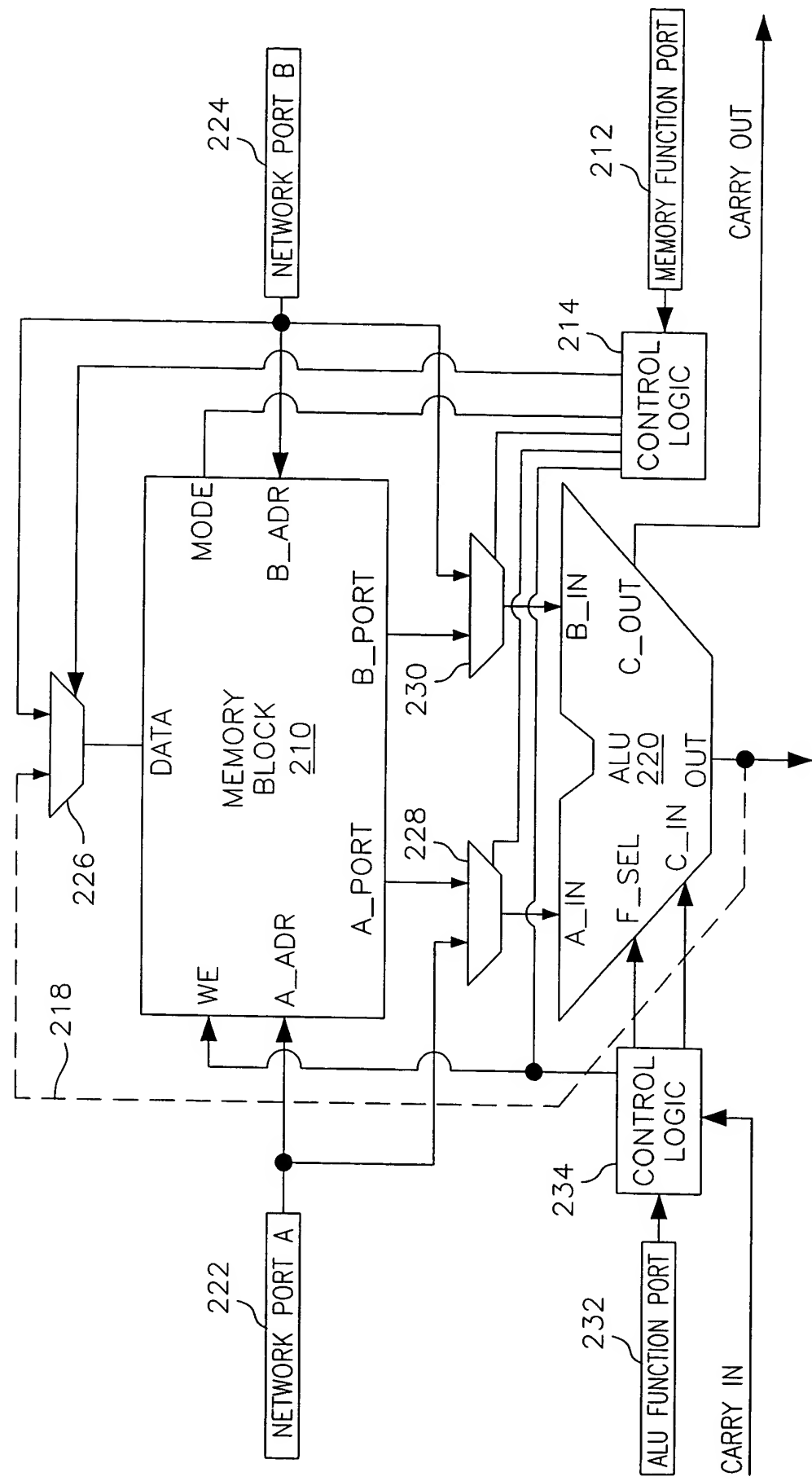


FIG. 3

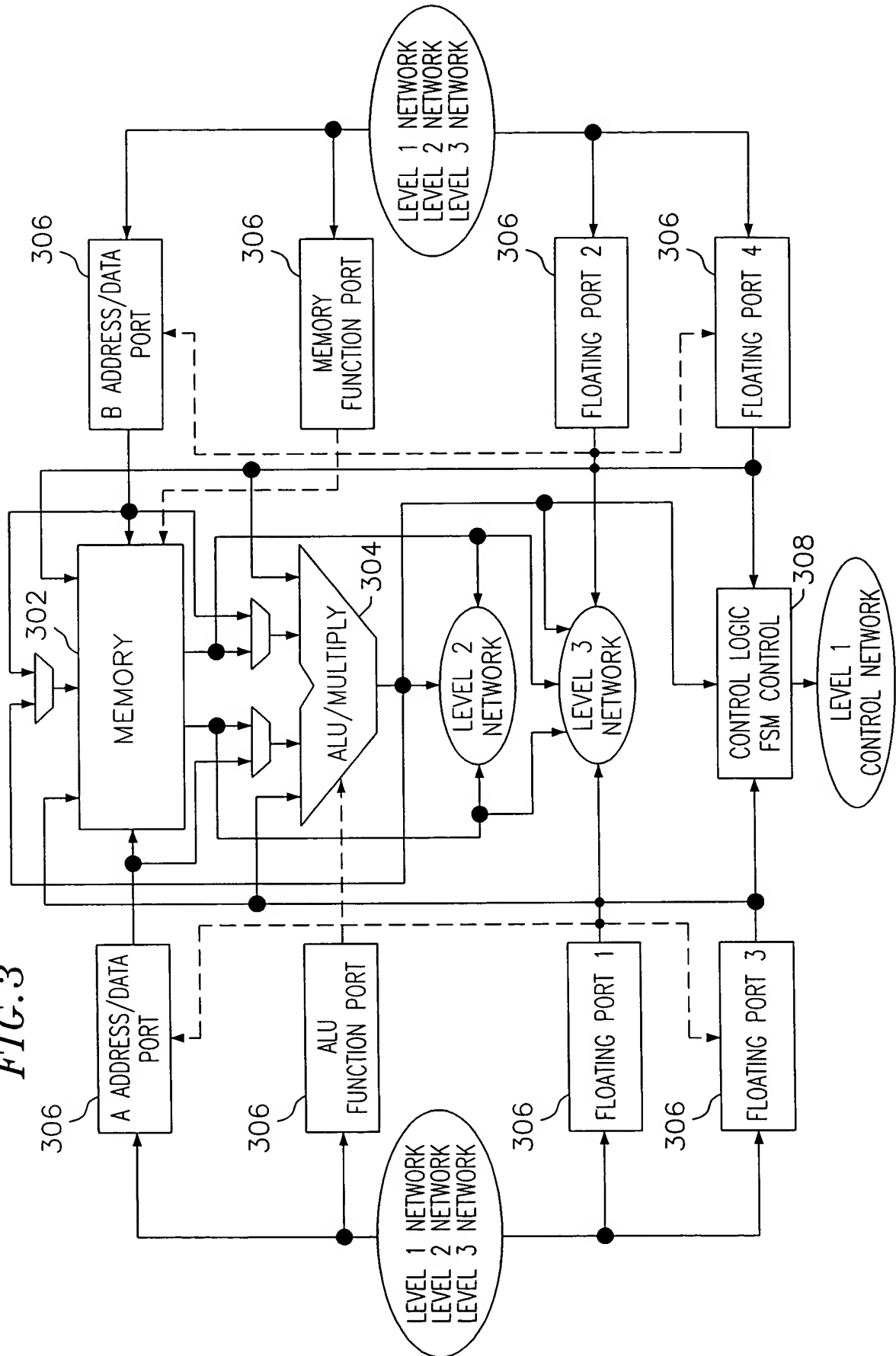


FIG. 4

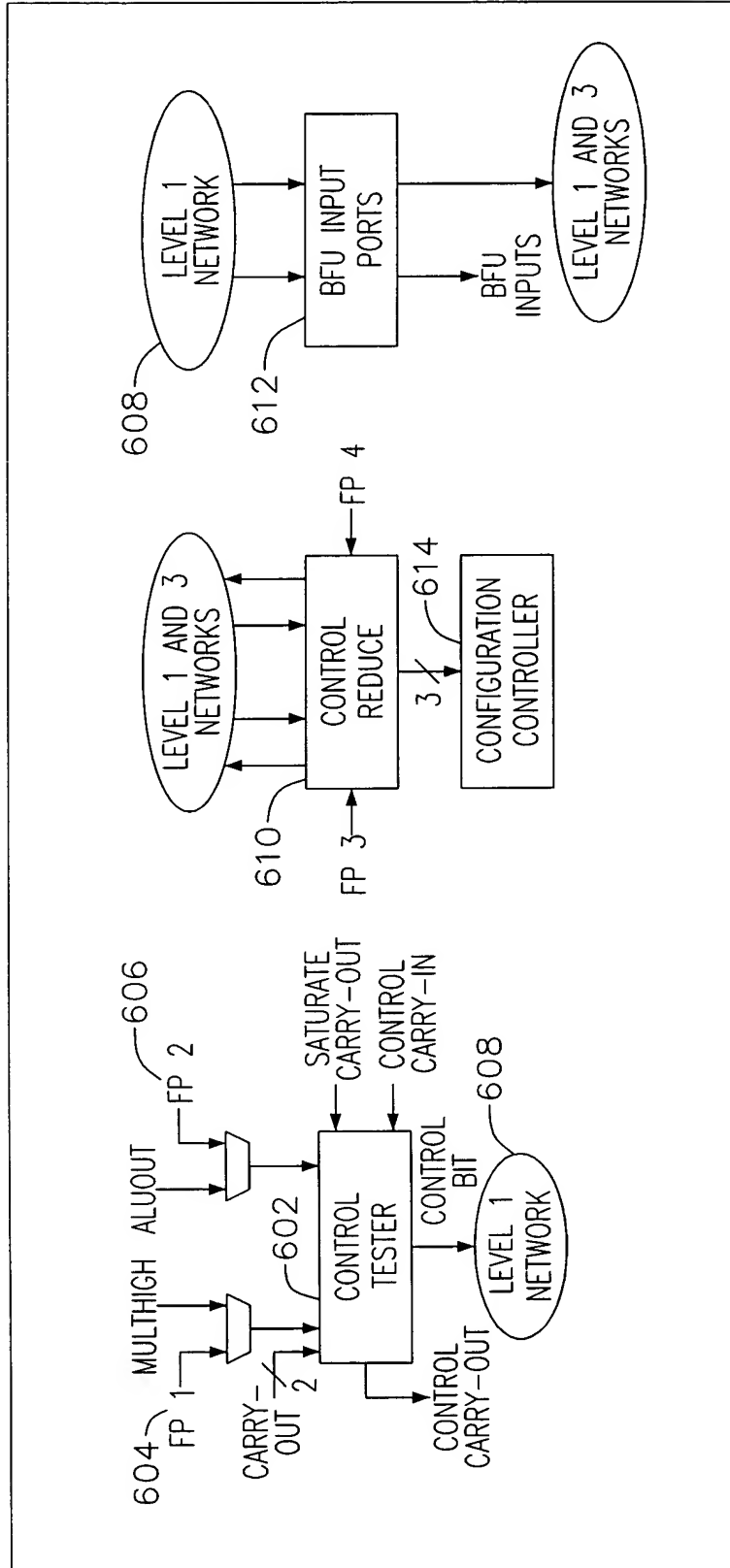


FIG. 5

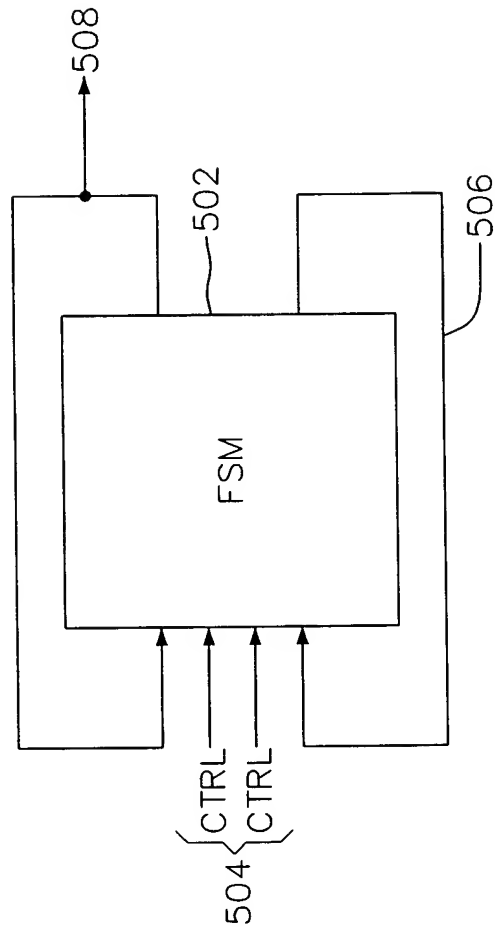


FIG. 6

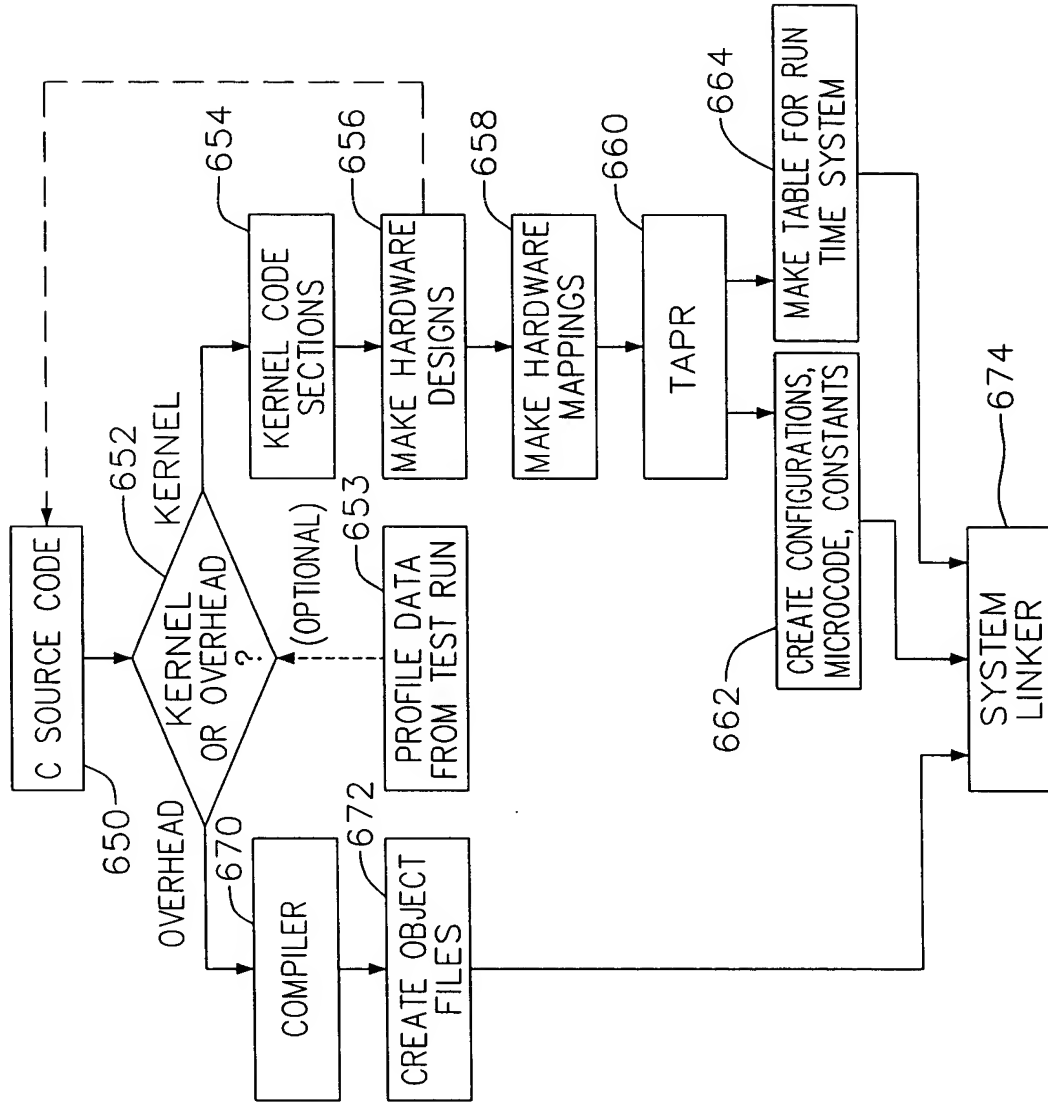


FIG. 7A

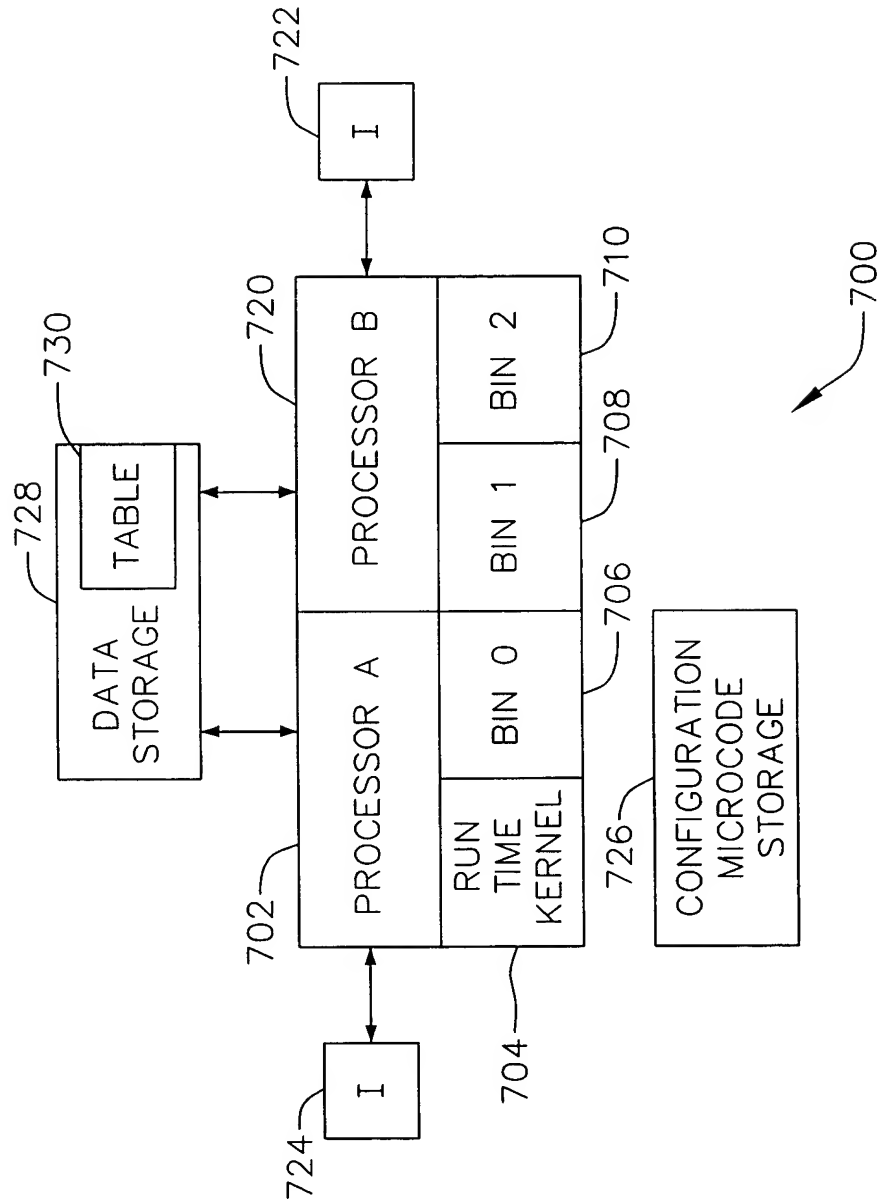


FIG. 7B

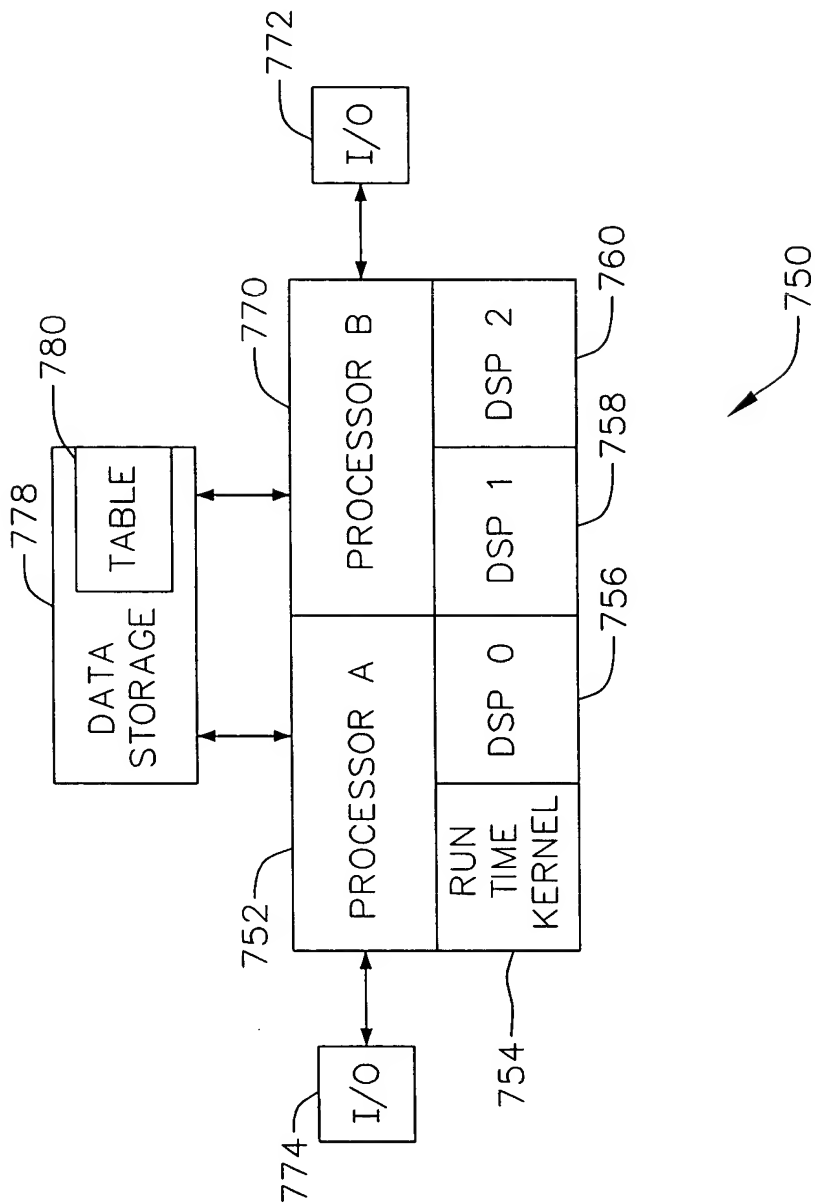
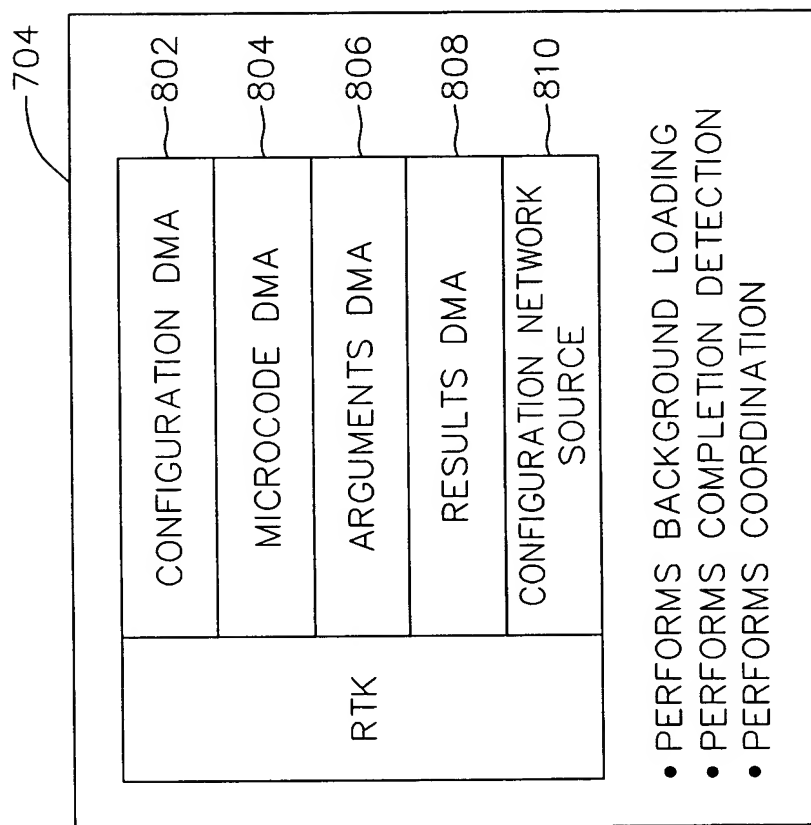




FIG. 8



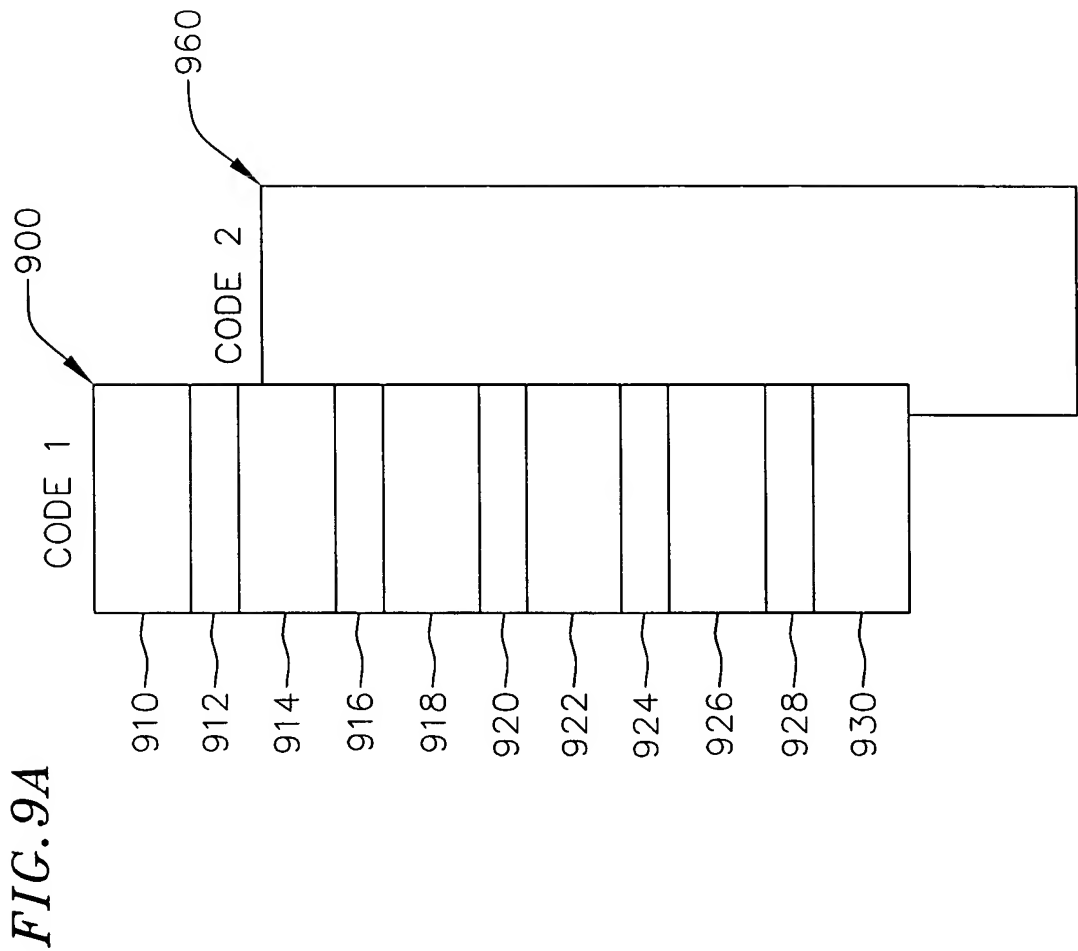


FIG. 9B

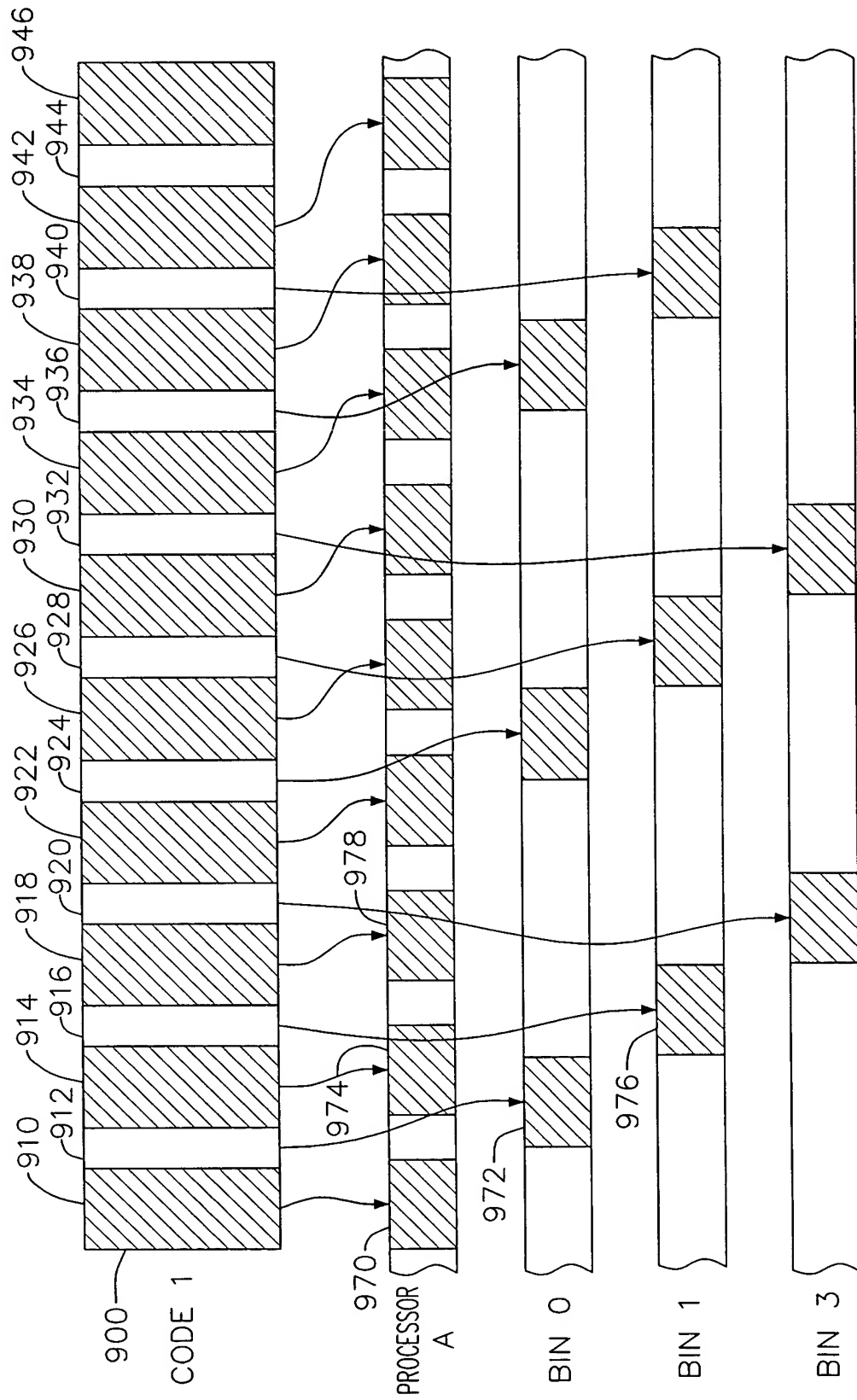
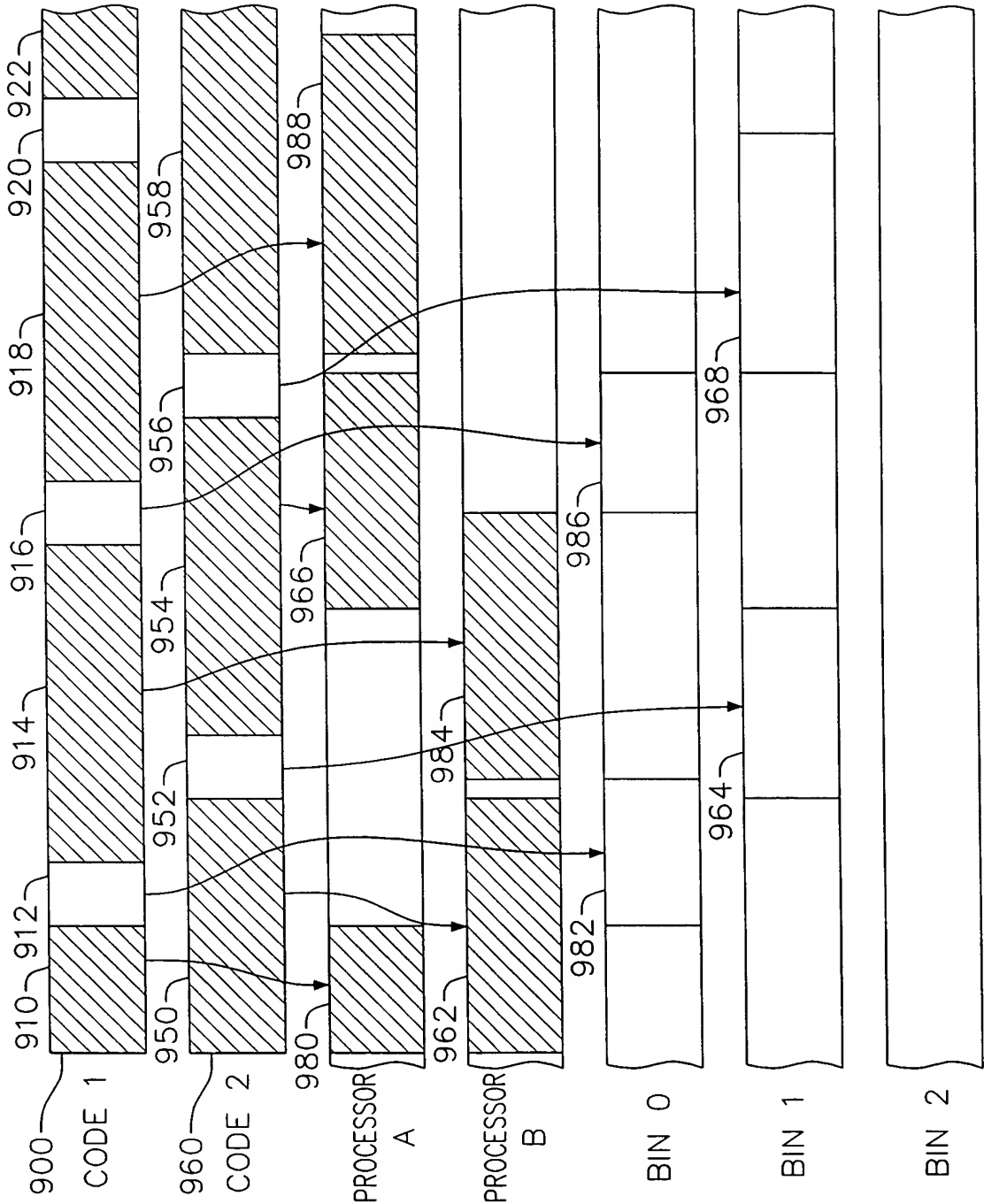


FIG. 9C



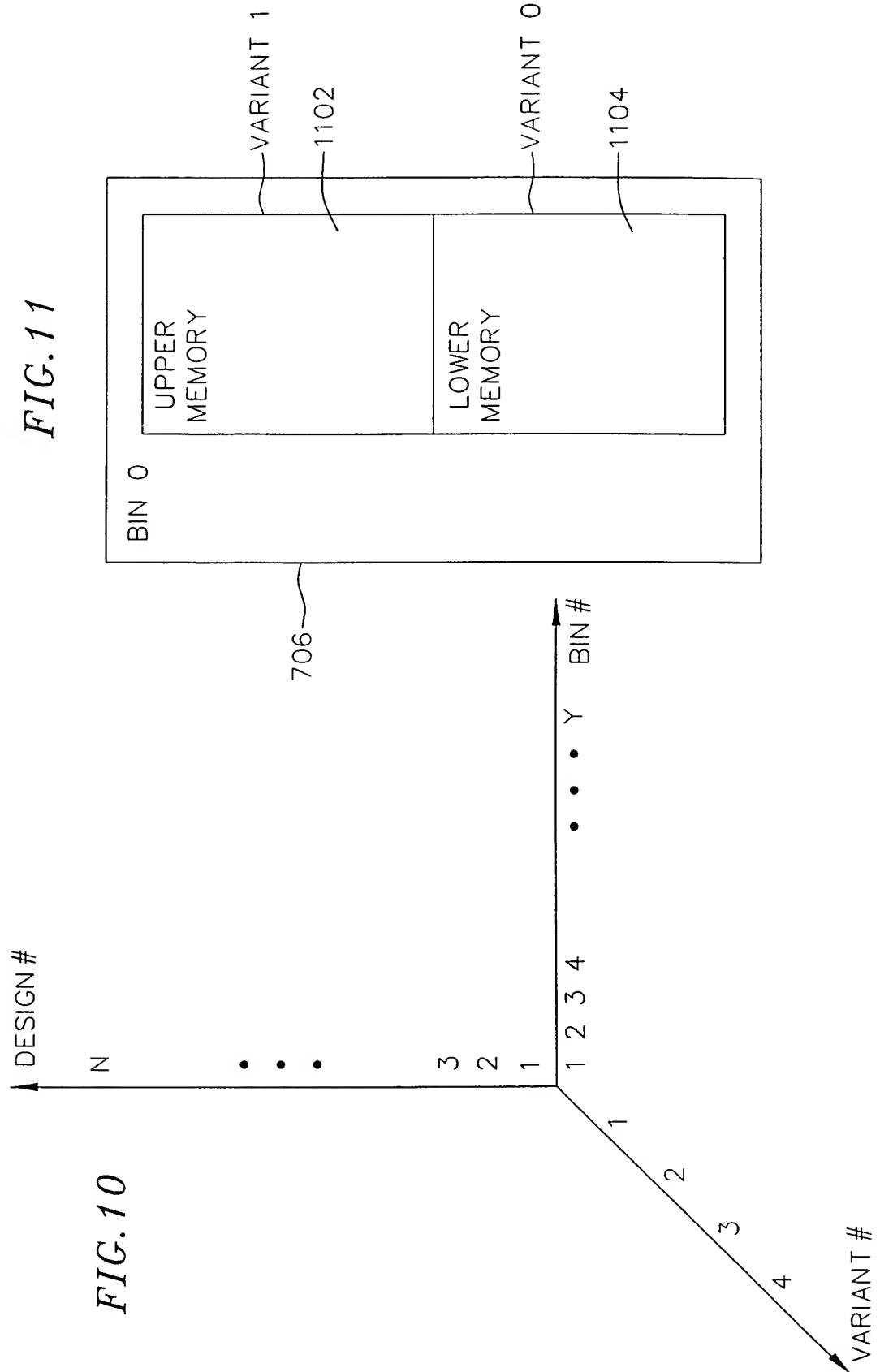


FIG. 12

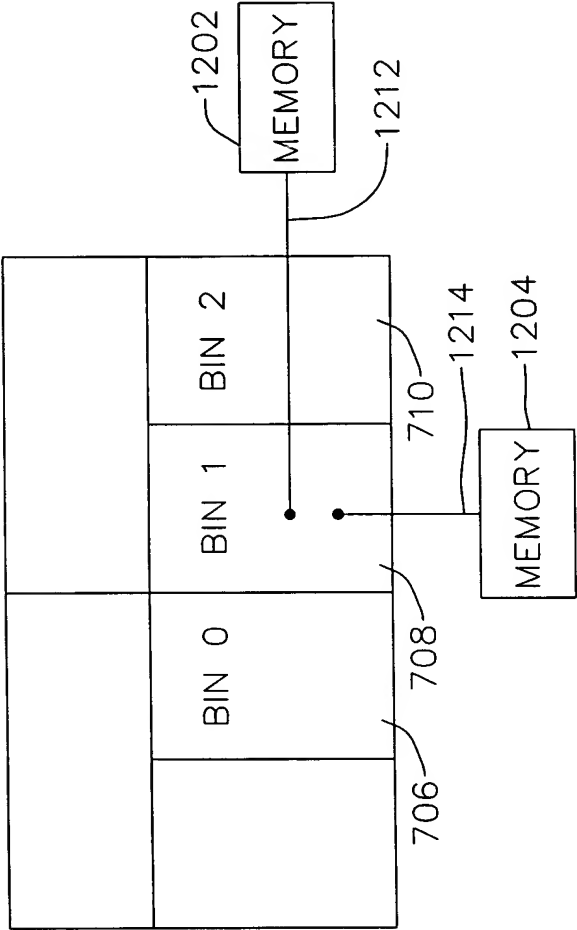




FIG. 14

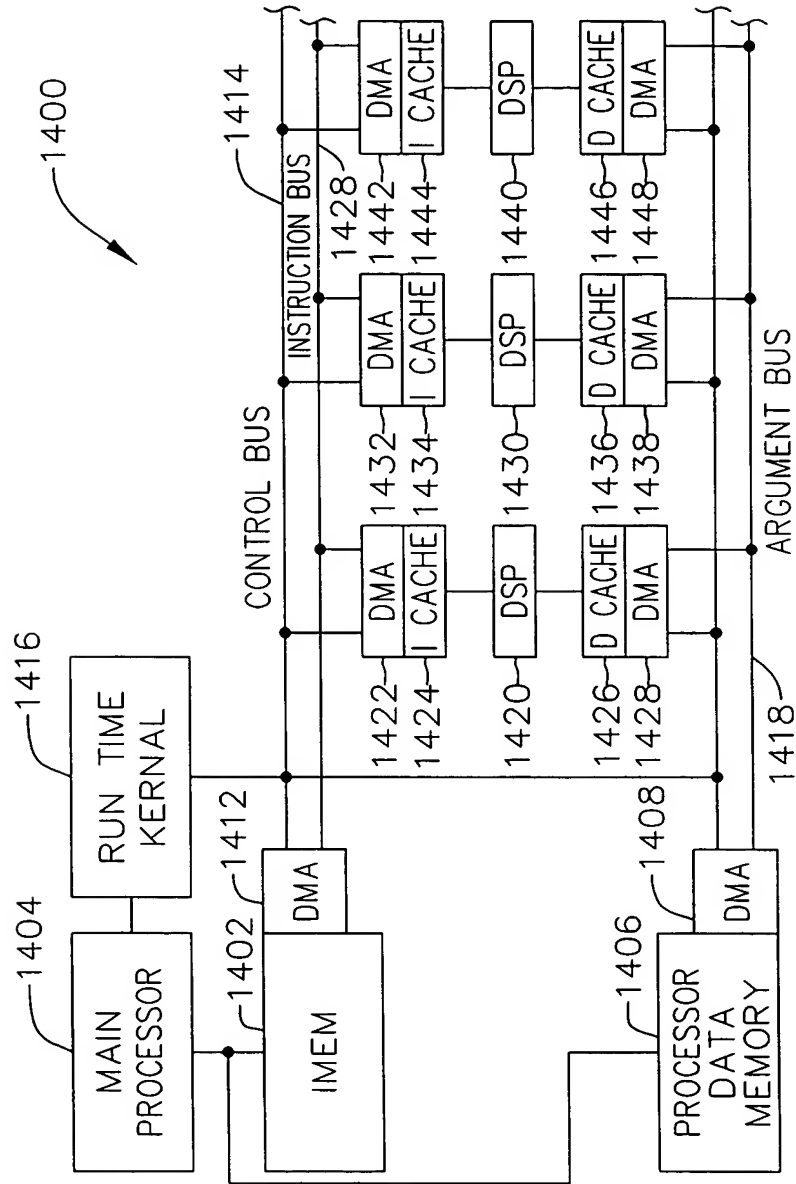




FIG. 15

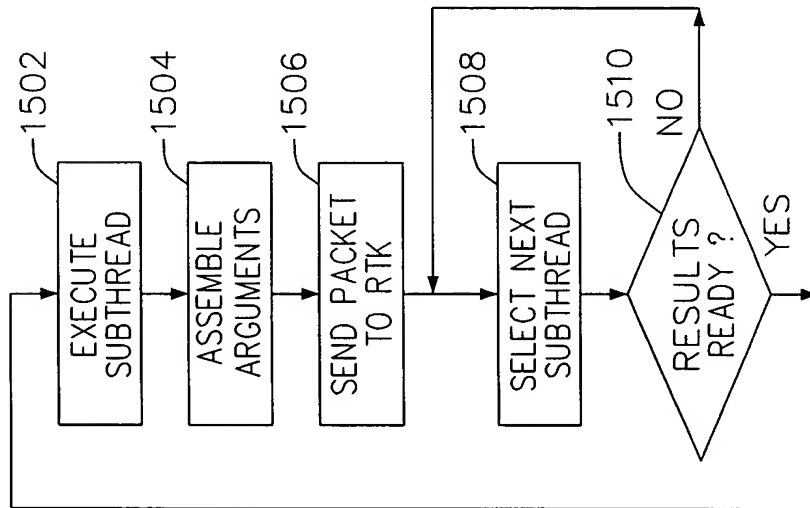


FIG. 16

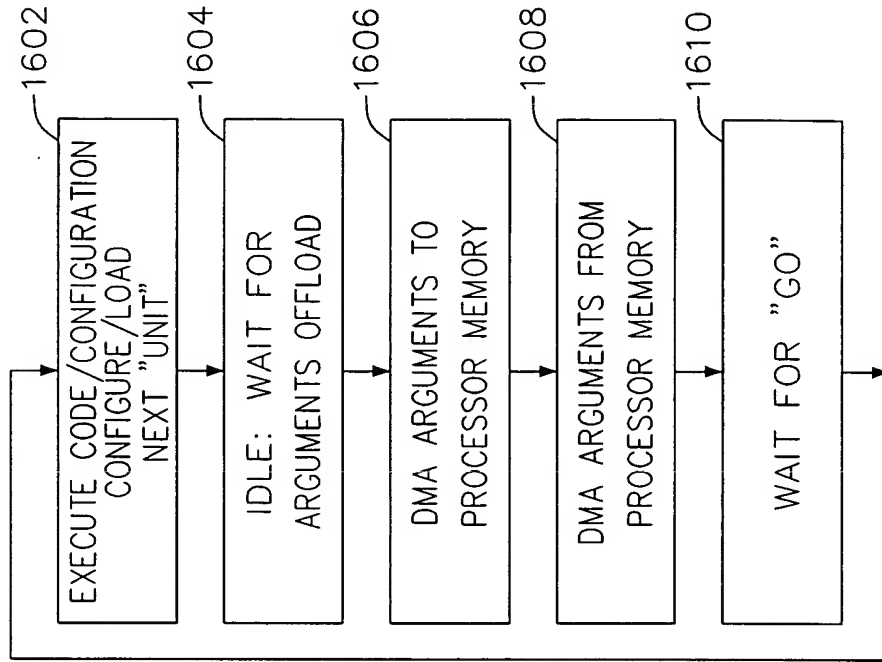


FIG. 17

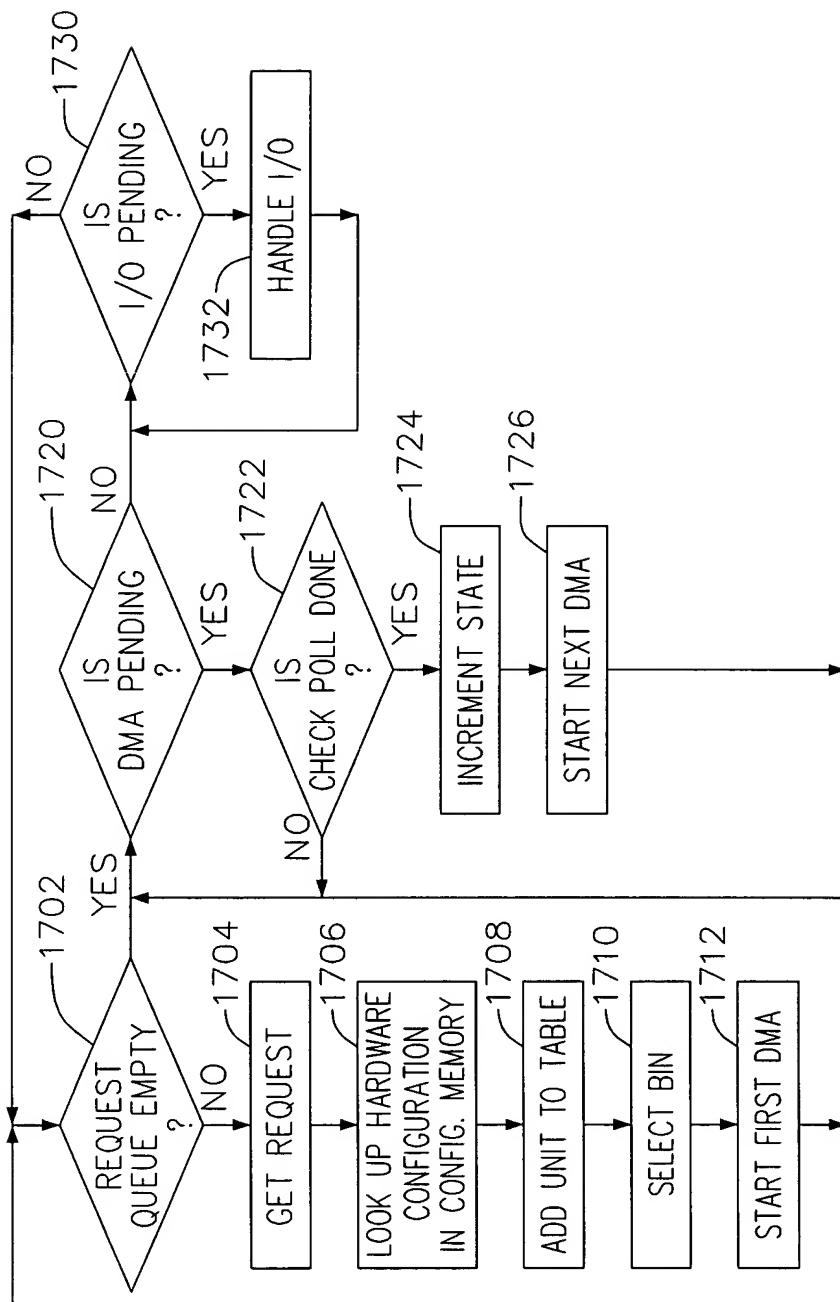


FIG. 18

1800

1802	1804	1806	1808	1810
HARDWARE ID	STATE	BIN	DMA PENDING	UNIT DONE
3	4	3	1	0

1820